11/10/05 11	:56 TEL 408 377 61	137	Xilinx Lega	1		Ø002/002
•	100	PART B	- FEE(S) TRA	NSMITTAL		
	NOV 1 0 2005		or Fax	Mail Stop ISSUE Commissioner fo P.O. Box 1450 Alexandria, Virg (571) 273-2885	r Patents inia 22313-1450	
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11/14/2005 TRECUOUD ASSASSAS STATE				Pat T	ompkins	(Depositor's name)
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APPLICATION NO.	FILING DATE	FIRST NAMED INVE		NTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
APPLN, TYPE	SMALL ENTITY	ISSUB F	EB I	UBLICATION FEE	TOTAL FEE(S) DUE	. DATE DUE
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TRAN, ANH Q		2819		326-041000	J	
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3. ASSIGNEE NAME A	ND RESIDENCE DATA TO	BE PRINTED ON	THE PATENT (print	or type)		
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P. O. Box 1450

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Inventor(s):

Shi-dong Zhou

Assignee: XILINX, INC.

Serial No.: 10/796,330

conf.no. 2114

Filed:

March 9, 2004

Title:

Structures and Methods of Implementing a Pass Gate Multiplexer with

NOV 1 0 -2005

Pseudo-Differential Input Signals

Docket No.: X-1660 US

Enclosed: Return Receipt Postcard

Fee Transmittal - Part B

Date: November 10, 2005

VIA FACSIMILE 571-273-2885

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